Product Development Capability for Creating Value

Developing products with new value helps to create new cultural trends. Casio's product development involves creating something from nothing —going from "0" to "1".

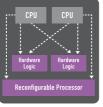
The Unique "Beautility" of **Creating Something from Nothing**

Casio's product development process emphasizes innovative planning. The only way to create new value that customers have never imagined is to think outside the box. The EXILIM EX-TR100, for example, features a bold design that completely transforms the conventional digital camera. With a rotating LCD and frame configuration, the EX-TR100 offers a totally new way to use a digital camera, maximizing the enjoyment of easy photo taking. This product design is distinctively "Casio," integrating beautiful design and highly utilitarian function.



Digital technologies Digital technologies make the impossible possible

The EXILIM Engine HS for digital cameras instantly performs complicated image processing via its reconfigurable processor, which enables operations to be resourced flexibly to the optimum hardware circuits, along with a multi-CPU design that performs decentralized processing at high speeds. This results in beautiful images with reduced noise, based or the automatic detection of photographic conditions such as backlighting or a night scene



The Five **Core Technologies: Constantly Advancing**

To develop products based on totally original ideas, Casio continually works to create advancements in five core technology areas.

Ease of use Easy for anyone to use

CASIO

CORE-

TECHNOLOGY

Casios high-end calculator models have a special feature that ensures reliable key punching. The surfaces of the keys for through 9 are concave, while the keys at the bottom such as "zero" have a gentle convex shape, making it easier to move th fingers across the keypad.



(patents)

Steady Acquisition of Intellectual Property

In order to strengthen its intellectual property, Casio puts great effort into patent applications and rights acquisition. The number of maintained patents in Japan

has been rapidly expanding in recent years (excluding patents that expire at the end of each year).

Total number of patents in force --- Ranking Based on Casio calculation for 9.372

Japanese companies, not including universities, research institutions. and foreian-owned firms



Taking on the Challenge of Next-Generation **Technology**

The new G-SHOCK equipped with Bluetooth Low subject lines of any emails received. In the future, Energy, a short-range wireless communication the watch will also be able to link up with health technology, is redefining what the wristwatch can devices, game machines, PCs and other everyday do. By linking this G-SHOCK to a smartphone via devices, thereby turning the wristwatch into a Bluetooth, for example, the wearer can use the multi-function information device. This will greatly watch to operate his or her phone, and view the expand the convenience of watches.

Energy saving Smartly operating

on little power

Casio has developed the industry's first all-band CMOS receiver LSI, which integrates the analog circuit that receives radio waves with the digital circuit that processes data. The LSI can be used for radio-controlled

02

03



watches, enabling them to maintain operational stability with an ultra-low power level that is 1/1,000th of that required by ordinary communication devices such as cellular phones. The technology has already been applied in Casio watches featuring Multiband 6 technology that can receive radio signals worldwide

Durabilit

A sense of security anywhere, anytime

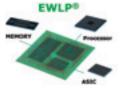
The glass face, buttons, and back case of every G-Shock watch are all designed not to make direct contact with the ground if dropped. Further, the heart of the watch is protected from shocks by a design that keep the internal module suspended in a hollow structure. Important parts inside the module are also protected by buffer materials



Compact size

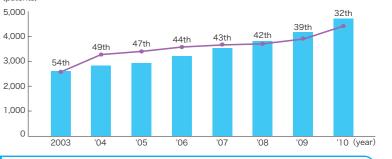
Compact, slim, lightweight

WLP*1 is a high performance, compact, highly reliable, low-cost, material-conserving semiconductor packaging technology. Applications of it are rapidly expanding, particularly for smartphones. Casio has also developed a high-density packaging technology, EWLP,*2 in which the WLP itself is embedded in the motherboard.



- *1.WLP: Wafer level package, an LSI package that enables rerouting of coppe traces, formation of electrode terminals, and encapsulation of chips in epoxy resin, all on an intact wafer. * 2.EWLP: Embedded wafer level package.

Number of maintained patents in Japan and ranking (not including patents that expired at the end of each year)



G-SHOCK Watch with Bluetooth LowEnergy **Interfaces with Smartphones**